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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Date: 3/10/2006
IN RE REISSUE APPLICATION OF: HALL, JEFFERSON W et al.
APPLN. NO.: 09/709893
FILED: 11/13/2000
FOR: POWER CONVERSION
INTEGRATED CIRCUIT AND METHOD FOR PROGRAMMING
Group: 2838
Examiner: Rajnikant B. Patel

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

S I R :

I hereby appoint practitioners named below:

Kevin B. Jackson, Reg. #38,502
Robert F. Hightower, Reg. #36,163

as attorney(s) to prosecute the above-identified application
and to transact all business in the Patent and Trademark
Office connected therewith.

I am the assignee of record of the entire interest. A
Statement under 37 CFR 3.73(b) is enclosed.

Respectfully submitted,

Bradley J. Botsch
Vice President and Chief
Intellectual property Officer
Semiconductor Components
Industries, L.L.C.
602-244-5603

*Total of 2 forms are submitted.



STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Jefferson W. Hall and Jade H. Alberkrack

Application No.: 09/709893 Filed On: November 13, 2000

Entitled:

POWER CONVERSION INTEGRATED CIRCUIT AND METHOD FOR PROGRAMMING

Semiconductor Components Industries, L.L.C., a limited liability company,
states that it is:

the assignee of the entire right, title, and interest in the patent application/patent identified above
by virtue of an assignment from the inventor(s) of the patent application/patent identified above.

The assignment was recorded in the United States Patent and Trademark Office at Reel 010776,
Frame 0122.

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Bradley J. Botsch
Vice President & Chief Intellectual Property
Officer
SEMICONDUCTOR COMPONENTS
INDUSTRIES, LLC

3-10-06

Date

602-244-5603

Phone